EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L7	20	"KURAKANE, HIROSHI"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/07/15 13:52
L8	27615	"NEC CORPORATION"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/07/15 13:52
L11	1	l8 and ((("frequency" adj ("multiplication" or "division") adj circuit$1) with "ratio") with (("n" or "N") with ("positive" adj "integer")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/07/15 14:02
L12	2	((("frequency" adj ("multiplication" or "division") adj circuit$1) with "ratio") with (("n" or "N") with ("positive" adj "integer")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/07/15 13:57
L13	26	((("frequency" adj ("multiplication" or "division")) with "ratio") with (("n" or "N") with ("positive" adj "integer")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/07/15 13:57
L14	13	l13 and (("clock" with control$3) with ("frequency" with circuit$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/07/15 14:03
L15	4747	375/376	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/07/15 14:01
L16	0	l15 and ((("frequency" adj ("multiplication" or "division") adj circuit$1) with "ratio") with (("n" or "N") with ("positive" adj "integer")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/07/15 14:03
L17	1832	331/16	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/07/15 14:02
L18	0	l17 and ((("frequency" adj ("multiplication" or "division") adj circuit$1) with "ratio") with (("n" or "N") with ("positive" adj "integer")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/07/15 14:03
L19	2520	327/158	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/07/15 14:03

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L20	0	I19 and (((("frequency" adj ("multiplication" or "division") adj circuit$1) with "ratio") with (("n" or "N") with ("positive" adj "integer")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/07/15 14:09
L21	4	"6346833"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/07/15 14:09
L22	2616	713/320	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/07/15 14:09
L23	0	I22 and (((("frequency" adj ("multiplication" or "division") adj circuit$1) with "ratio") with (("n" or "N") with ("positive" adj "integer")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/07/15 14:09

ratio frequency multiplication with positive inte[   ] | **Search Patents** | Advanced Patent Search
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Patents

Patents **1 - 10** on **ratio frequency multiplication with positive integer**. (0.25 seconds)

Frequency multiplier circuit
US Pat. 6346833 - Filed Aug 18, 2000 - OKI Electric Industry Co., Ltd.
A method of **frequency multiplication** comprising: dividing a **frequency** of a ...
wherein n is a **positive integer**; counting a number of pulses Y in the divided ...

Phase locked loop circuit with high stability having a reset signal generating circuit
US Pat. 6066988 - Filed Aug 14, 1998 - NEC Corporation
By such a function, the PLL circuit can output the output clock signal which has
the **frequency** N (N is a **positive integer**) times more than the **frequency** of ...

Digital open loop programmable **frequency** multiplier
US Pat. 4244027 - Filed Mar 19, 1979 - RCA Corporation
The **multiplication** factor is the **ratio** of the frequencies of the two reference .
... to multiply by any **positive** real number whether or not of **integer** value. ...

Digital **frequency** multiplier
US Pat. 4339722 - Filed May 20, 1980 - Micro Consultants Limited
1 and to rely on the modifiers to provide the desired **ratio** for N. In ...
second path to allow a **multiplication** rate selected to be other than an **integer**. ...

Frequency generation for multiplexed analog component color television encoding and decoding
US Pat. 4652903 - Filed Sep 21, 1984 - Scientific Atlanta, Inc.
Nevertheless, neither compression **ratio** is so high that an unacceptably ...
Because no **frequency multiplication** is involved (which would be the case if ...

Integrated circuit and method for low noise **frequency** synthesis
US Pat. 6107891 - Filed May 6, 1999 - Applied Micro Circuits Corporation
Preferably, the **positive integer** is 2. The preferred embodiment of the **frequency**
multiplier, which multiplies the **frequency** of the reference signal by 2, ...

Video system for conveying digital and analog information
US Pat. 3984624 - Filed Jul 25, 1974 - Weston Instruments, Inc.
Divider 14 is selected to provide an negative-going ramp for a negative average
level, as even-**integer** division **ratio** so that there will always be shown ...

Carrier **frequency** error detector capable of accurately detecting a carrier **frequency** error
US Pat. 5276710 - Filed Jul 30, 1992 - NEC Corporation
(2) EMBODIMENT In order to detect the carrier **frequency** error ft in the ...
m mined **integer** N is usually equal to 2m where m represents a **positive integer**. ...

Method and apparatus for deriving pseudo range from earth-orbiting satellites
US Pat. 4797677 - Filed May 19, 1986 - Istac, Incorporated
10 shows **integer** generator 118 in more detail, five-sixths of a wavelength, XL,
... The outputs of phase detector 134 and a **frequency** counter 136. ...

BOLIE
US Pat. 2829255 - Filed Oct 10, 1955
A **frequency** synthesizer system including harmonic generating means for ...
R being the radix of the digital system, and M being a **positive integer**, ...

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